

## ABSTRACT

A microcontroller operating in synchronization with clock includes: an arithmetic unit operating in synchronization with the clock; an internal resource being  
5 connected to the arithmetic unit via a bus, and having at least a bus interface, and an internal circuit operating in synchronization with the clock; and a system resource prescaler which generates from the clock an operation permission signal denoting an operation permission state  
10 in  $m$  cycles out of  $n$  cycles of the clock ( $m \leq n$ ), and supplies the operation permission signal to the internal circuit of the internal resource. The above-mentioned internal circuit operates in synchronization with the clock when the operation permission signal denotes the operation  
15 permission state.